Ref.

KMS-92QSP24

Referrence Material

October, 15, 2009

Note that no revised edition

Specifications

Product name: MAR-M35X-NN51

(Metal disk, Battery separate cord, 17-bit full ABS)

Nikon Encoder Business Div product code: E J A 3 5 2 1 8 (Sensor)

E J B 3 3 0 0 0 (Disk)

Nikon Instruments company product code: EXK29852 (Sensor)

EXK29853 (Disk)

Specification number: 06ES033A

Your part name:

ightharpoonup Ref. KMS-92QSP24

Oct. 9, '09	<u>←</u> × Initial edition	
	$\triangle \times$	
	$\triangle \times$	
	$\triangle \times$	
	Δ×	
	$\triangle \times$	
	$\triangle \times$	
	$\triangle \times$	
	Δ×	
	$\triangle \times$	
	$\triangle \times$	
	Δ×	
	$\triangle \times$	
	Δ×	
_	$\triangle \times$	
	$\triangle \times$	
_	$\triangle \times$	
	$\triangle \times$	
	Δ×	
	$\triangle \times$	
	$\triangle \times$	
	Δ×	
年 月	∃ △×	
年 月	∃ ∆×	
年 月	Ħ △×	

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

Overview	
Features	
Basic specifications	4
3. 1 Resolution	
3. 2 Response turn speed	
3. 3 Classification of operation states 3. 4 Multi-turn calculation function	
Mechanical specifications	5
4. 1 Shaft inertia moment	O .
4. 2 Allowable turn angle acceleration	
4. 3 Mass	
4. 4 Outside drawing	
4. 5 Specification for mounting shaft	_
Electrical specifications	8
5. 1 Maximum absolute rating	
5. 2 Electrical characteristics5. 3 Specifications for single turn	
5. 4 Specifications for multi turn	
5. 5 Backup portion	
Serial communication	1 1
6. 1 Overview	
6. 2 Frame format	
6. 3 Field detail	
Functional explanation of status flags	1 9
Detection of data capture frame	2 0
Detection of E 2 PROM access frame D. Instruction in request transmission	
J. Instruction in request transmission	
1. 1. 1 Temperature	∠ 3
1 1. 2 Humidity	
1 1. 3 Vibration	
11.4 Impact	
11. 5 Noise resistance	
2. Configuration example of transmitting/receiving circuits	2 4
3. I/O signal	2 4
4. Power supply circuit (for reference)	2 5
5. Serial number plate6. Serial number plate6. Signal adjustment standard	
o. Signal adjustment standard7. 7. Assembling adjustment procedure7.	
7. Assembling adjustment procedure	2 /
17. 1 Mounting purse disk 17. 2 Mounting base plate	
17. 3 Signal adjustment	
17. 4 CS adjustment	
17.5 Signal check	
3. Packing specifications	2 9
18. 1 Pulse disk	
18. 2 Base plate	
18. 3 Other parts	_
9. Usage precautions————————————————————————————————————	3 1
). Design revision record	3 2
年 月 日 △×	
年 月 日 △×	
F 月 日 △×	

 $|_{
m Ref.}|$ KMS- 9 2 Q S P 2 4

1. Overview

This absolute encoder is a modular type 33-bit electrical multi-turn absolute encoder that outputs absolute position information within a 17-bit single turn and 16-bit multiple turn position information obtained from a battery backup counter counting a magnetic incremental pattern for 1 pulse per turn, and is mainly mounted to AC servo motor.

2. Features

- · Compact design with diameter of 35mm and single PWB structure
- · Approx. 0.5 mm wide gap in between pulse disk and light sensors allows for easy mounting to motor
- MR sensor is used for multi-turn detection and thus, high speed response is possible upon backup operation

In addition, there is virtually no restriction on turn angle acceleration

Possible to write any data into E²PROM

3. Basic specifications

3.1 Resolution

• Single turn : 2^{17} (1 3 1 0 7 2 P/R) Absolute data

• Multi- turn : 2^{16} (6 5 5 3 6 turns)

3.2 Response turn speed

• Normal operation : $6000 \, \text{m i n}^{-1}$

• Backup operation : $10000 \,\mathrm{m}$ i n^{-1}

3.3 Classification of operation states

Three operation states are available as below depending upon power supply.

State	Main power supply voltage Vcc (TYP)	External battery power supply voltage (TYP)	Built-in backup capacitor voltage (TYP)	
Normal operation	4. $75 \sim 5$. 25 V	_	_	
D 1	0.17	2. 76 V~4. 0 V	_	
Backup operation	0 V	2. 76 V or less	2. 76 V or more	
Non-operation	0 V	2. 76 V or less	2. 76 V or less	

3.3.1 Normal operation states

The normal operation makes the count operation of single turn and multi-turn data and transmission/reception of data by serial communication possible.

Transmission/reception of data is possible after maximum 500ms or more elapses since main power is ON.

When the backup operation is switched over to the normal operation (upon turning on power), the switchover should be carried out with turn speed 300min⁻¹ or less. When the encoder turn speed is 300min⁻¹ or more at the time of turning on main power, scanning of M-series pattern may not be normally conducted and in this case, the absolute position data cannot be determined and thus, BUSY flag is shown. When the turn speed is lowered and the scanning returns to normal state, BUSY flag is automatically cleared and the absolute position data is determined. However, when the normal scanning is conducted even if the turn speed is 300min⁻¹ or more, BUSY flag is cleared.

(Reference: Actual value of turn speed capable of scanning: Approx. 700min⁻¹)

年	月	日	$\triangle \times$
年	月	日	$\triangle \times$
年	月	月	$\triangle \times$

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

3.3.2 Backup operation states

The backup operation permits to perform multi-turn count operation by external battery or built-in backup power source. In this case, the data is neither transmitted nor sent.

3.3.3 Non-operation states

All operations of the encoder are stopped. In this state, MT error flag is latched inside the encoder and the data is transmitted externally after main power is turned on.

4. Mechanical specifications

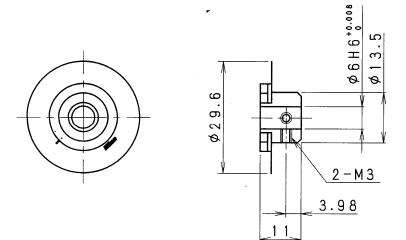
4.1 Moment of shaft inertia (calculation value)

4.
$$1 \times 10^{-7} \,\mathrm{kg \cdot m^2} \,(4. \, 1 \,\mathrm{g \cdot cm^2})$$

4.2 Allowable turn angle acceleration

1.
$$0 \times 10^5$$
 r a d/s e c²

- 4.3 Mass 0. 1 kg
- 4.4 Outside drawing
 - (1) Outside drawing of pulse disk

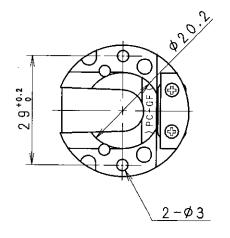


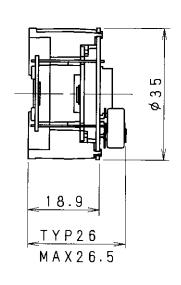
Accessory M3 Hexagon socket set screws (1 = 3) : 2 pieces

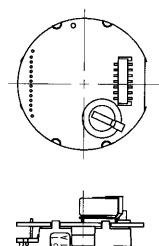
年	月	日	$\triangle \times$

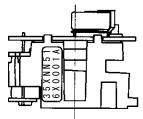
 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

(2) Outside drawing of Sensor









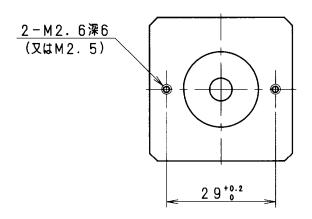
年	月	日	$\triangle \times$

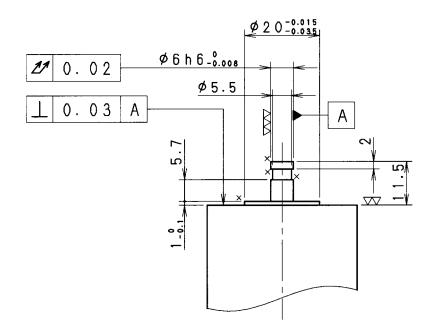
年 月 日 △×

年 月 日 △×

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

4. 5 Specification for mounting shaft





X: Chamfering with 0.2 through 0.3 is required. Thrust variation must be $\pm\,0.2$ or less.

年	月	日	$\triangle \times$
	刀	Н	\triangle \wedge

年 月 日 △×

年 月 日 △×

 $|_{
m Ref.}|$ KMS- 9 2 Q S P 2 4

5. Electrical specifications

5.1 Maximum absolute rating (Temperature range on the PWB : $-20{\sim}+85{^\circ}{\rm C}$)

Item	Mark	Rating	Unit
Main power supply	Vcc	$-0.3 \sim 6.0$	V
voltage External battery power	Vbat	$-0.3 \sim 6.0$	V
supply voltage	V Sac		,

5.2 Electrical characteristics (Temperature range of undesignated PWB : $-20 \sim +85 ^{\circ}\text{C}$)

Τ.	0 11.1		11 .		
Item	Condition	MIN	TYP	MAX	Unit
Main power supply voltage	Normal operation (ripple 1% or less) *5)	4.75	5.0	5.25	V
External battery power supply voltage	Backup operation *5)	3.0	3.6	4.0	V
Normal operation←→	Normal→Backup *5)	4.05	4.15	4.25	V
Backup operation	Backup→Normal *5)	4.14	4.31	4.47	V
MT error occurrence voltage	*1)	2.66	2.76	2.93	V
Battery alarm occurrence voltage	External battery voltage *5)	2.94	3.0	3.06	V
Possible Data transmission start time	Power impedance = 0 After switching from backup→normal operation			0.5	sec
Response turn speed	Non-operation → Normal Backup→Normal *2)			300	${\tt min}^{-1}$
	Normal operation			6000	\min^{-1}
	Backup operation			10000	\min^{-1}
Current consumption in normal operation	Capacitor discharge *3)		130	170	mA
(VCC=5.0V when line is opened)	Normal operation		120	160	mA
Normal operation Current consumption of external battery			1.0	3.4	μ A
Backup operation Current consumption of external battery	After charged for more than 10 minutes at 20°C ambient temperature and Vcc=5V		30	35	μ A

年	月	日	$\triangle \times$	
年	月	月	$\triangle \times$	
年	月	日	$\triangle \times$	

Backup t			_		I	Ref. KMS-92QSP24		
	ime	After charged for more than 3 hours at Vcc=5V *4)	3. 5			hour		
2) It is the turn s3) The current countries and when interterminals are s4) The value is on Life of built-in	speed value that or onsumption is the rnal backup capae short-circuited. one upon product a capacitor is subj	ner voltage value of backup but does not turn on BUSY flag where instantaneous maximum currector is charged, the operation is shipment as well as upon shaft ject to change depending upon at power supply terminals on ending the property of	en main power is ON ent when main power is changed to normal stop. ambient temperature	N while turning. This turned on with the lackup	internal backup cap capacitor is not di	ischarged even if		
月 月 月 日								

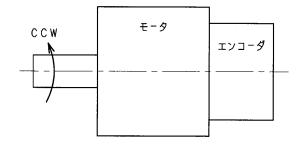
 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

5.3 Electrical specifications within single turn

	Item	Specification	Remarks
Res	solution	2 ¹⁷ ST[16:0]	Addresses 0 \sim +131071
Max turn speed Normal operation		$6000 \mathrm{min}^{-1}$	
Max angle acceleration	Normal operation		Due to mechanical restriction
Out	put code	Pure binary code	
Directio	n of increase	CCW	Please refer to following diagram
(after interpol	r for interpolation lation correction is ermined)	± 1 0 ″	Phase interpolation corrective operation works when approx. 3.5° or more is turned after main power is turned on.
Repeat accurac	reproducibility by with main power rned on	±1LSB	Position reproducibility upon power ON-OFF turns out $\pm120''$ (max).

5.4 Multi-turn signals electrical specifications

R	emark	Specification	Remark
Res	solution	1 Count/Turn	_
Max mult	i-turn count	2 ¹⁶ MT[15:0]	65536 turns
	Normal operation	6000 min ⁻¹	
Max turn speed	Backup operation	$10000~{\rm min}^{-1}$	_
Max angle	Normal operation	$1.0 \times 10^5 \text{ rad/s}^2$	Due to mechanical restriction
acceleration	Backup operation		
Output code		Pure binary code	
Directio	on of increase	CCW	Please refer to following diagram



CCW denotes the direction when motor shaft or encoder shaft is turned in the anti-clock direction with motor kept stationary.

年	月	月	Δ×
年	月	日	$\Delta \times$
年	月	日	$\Delta \times$

 $_{\mathrm{Ref.}}$ KMS- 9 2 Q S P 2 4

5.5 Backup power supply

5.5.1 Internal backup power supply (electric double layer capacitor)

(1) Definition of backup time

The backup time means a state of the battery being not connected. More specifically, it denotes the time until Vcc terminals are opened and data of multi-turn count amount cannot be stored (changed to non-operation state) since the normal operation is getting under way with DC5V supplied to Vcc for more than 3 hours.

(2) Capacitor charging circuit

Capacitor is charged from the main power supply (DC5V) and external battery power through diode • resistor.

(3) Capacitor life

Life of the electrical double layer capacitor is subject to change depending upon ambient temperature. Beware of high humidity atmosphere.

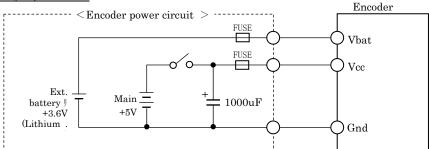
5.5.2 External backup power supply (External battery)

- (1) Battery guaranteeing proper operation: 2000mAh Lithium battery ER6 (3.6V) made by Toshiba
- (2) Recommended power supply circuit (for reference)

For fail safe against voltage delay (%VD) of lithium battery, electric capacity with approx. 1000 μ F is recommended to be provided at Vcc line (5V) of the encoder power supply circuit (please refer to following block diagram.

*What is all about VD···When the battery has not been in use for a long period of time or has been used with feeble electric current, film is generated on anode surface, thereby producing the phenomenon that the voltage will temporarily fall when the large current is supplied.

Battery separate cord



5.5.3 Backup power supply switchover

The power is supplied in order of higher voltage of Vcc, external battery and internal backup capacitor. Unless difference in voltage exceeds over a forward voltage of switching diode of switching circuit, however, the power is not properly switched over.

年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	

m Ref. KMS- 9 2 Q S P 2 4

6. Serial communication

6.1 Overview

Item	Specifications	Remarks	
Communication code	Binary code	_	
Transmission system	Differential line driver	Based upon RS 485	
Reception system	Differential line receiver	Based upon RS 485	
	Single turn data	17 bit	
	Multi-turn data	16 bit (0~65535)	
		(1) Overspeed	
		(2) BUSY flag	
Transmission data		(3) ST error	
	Status flag	(4) Overflow flag	
		(5) PS error	
		(6) Battery alarm	
		(7) MT error	
Synchro system	Baseband NRZ	_	
Baud rate	2.5 Mbps	_	
Frame format	Detail shown at paragraph 6.2 and on	_	

$6.1.1 \quad E^{\scriptscriptstyle 2}PROM$

Item	Specifications	Remarks		
Accessible addresses	0 through 79	Data is unassigned upon		
		shipment		
Number of allowable	100000 in total	One access for writing =		
writings		single writing		

6.2 Frame format

6.2.1 Encoder data capture

Control field

年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	

 $_{\mathrm{Ref.}}$ KMS- 9 2 Q S P 2 4

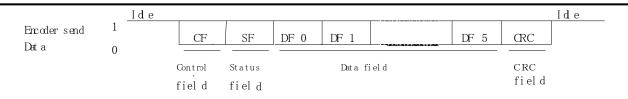


FIG. 1 Encoder data capture frame format

6.2.2 E²PROM access (writing)

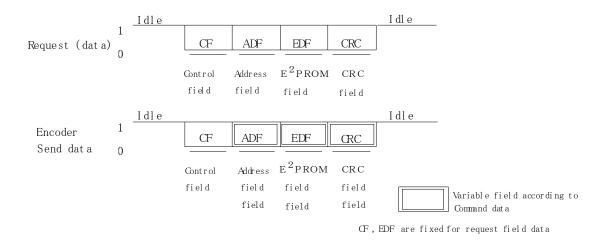


FIG. 2 Writing frame format

6.2.3 E²PROM access (reading)

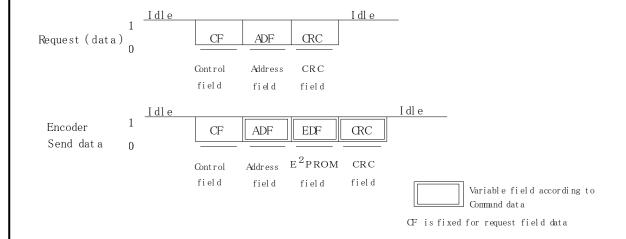


FIG. 3 Reading frame format

年	月	日	$\triangle \times$
年	月	日	$\triangle \times$
年	月	日	$\triangle \times$

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

6.3 Field detail

6.3.1 Control field (CF)

Configuration of control field is as shown in FIG.4.

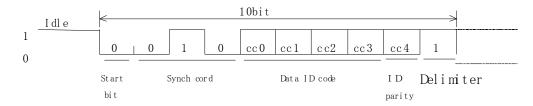


FIG.4 Configuration of control field

① Start bit : Fixed

② Synch cord : Fixed_o

③ Data ID code : Data shown in Table 2 is outputted from encoder by specifying

data ID code shown in Table 1.

When specifying data ID code, please follow the application of Tabel 1. (For example, data ID code for reset should not be

used for data capture.)

④ ID parity : Parity of data ID code

⑤ Delimiter : Fixed

Table 1 Data ID code chart

Application	Data ID		Parity			
Application	Data ID	cc0	cc1	cc2	cc3	cc4
	Data ID 0	0	0	0	0	0
Data capture	Data ID 1	1	0	0	0	1
Data capture	Data ID 2	0	1	0	0	1
	Data ID 3	1	1	0	0	0
Data writing	Data ID 6	0	1	1	0	0
Data reading	Data ID D	1	0	1	1	1
	Data ID 7	1	1	1	0	1
Resetting	Data ID 8	0	0	0	1	1
	Data ID C	0	0	1	1	0

-	年	月	日	$\triangle \times$
	年	月	日	$\triangle \times$
	年	月	日	$\triangle \times$

 $_{\mathrm{Ref.}}$ KMS-92QSP24

6.3.2 Status field (SF)

Configuration of status field is as shown in FIG. 5.

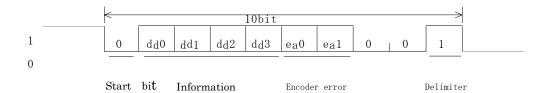


FIG. 5 Configuration of status field

① Start bit : Fixed

② Information : All fixed to "0".

③ Encoder error : Logical value "1" is outputted respectively upon each error occurrence.

Bit	ea0	ea1
Logic upon error occurrance	1	1
Content	ST error	PS error, Battery alarm, MT error and BUSY flag are outputted in OR logic.

④Delimiter : Fixed

 年 月 日 △×

 年 月 日 △×

 年 月 日 △×

 $_{\mathrm{Ref.}}$ KMS- 9 2 Q S P 2 4

6.3.3 Data field (DF0~DF5)

Relationship between data ID code and data field is as shown in Table 2.

Table 2 Data field chart

Data ID	DF 0	DF 1	DF 2	DF 3	DF 4	DF 5	DF6	DF7
Data ID 0	ABS 0	ABS 1	ABS 2					
Data ID 1	ABM 0	ABM 1	ABM 2					
Data ID 2	ENID							
Data ID 3	ABS 0	ABS 1	ABS 2	ENID	ABM0	ABM1	ABM2	ALMC
Data ID 7	ABS 0	ABS 1	ABS 2					
Data ID 8	ABS 0	ABS 1	ABS 2					
Data ID C	ABS 0	ABS 1	ABS 2					

Blank denotes no transmission data avaiable.

ABS0~ABS2: Absolute value data within single turn

Of all 24-bit frames, let ABS0 be low byte and ABS2 be high byte. High 7-bit of ABS2 is 17-bit data of full-time logical value "0".

ABM0~ABM2 : Multi-turn data

Of all 24-bit frames, let ABM0 be low byte and ABM2 be high byte.

ABM2 is 16-bit data of full-time logical value "0".

ENID : Encoder ID (= 2 3 H fixed)

ALMC : Encoder error (Please refer to Table 3)

Table 3 ALMC

Bit	d ₇ 0	d ₇ 1	d_72	d_73	d_74	d_75	d76	d ₇ 7
Logic upon error occurrance	1	1	1	1	_	1	1	1
Naming	Overspe ed	BUSY flag	ST error	Overflow flag	Fixed to '0'	PS error	MT error	Battery alarm
	OVSPD	BUSY	STERR	OVF		PSERR	MTERR	BATT

年 月 日 △×

年 月 日 △×

年 月 日 △×

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

Configuration of each data field is as shown in FIG.6.

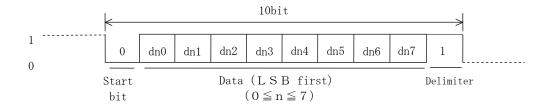


FIG.6 Configuration of data filed

① Start bit: Fixed

② Data : Configured in accordance with LSB first.

3 Delimiter : Fixed

6.3.4 CRC field (CRC)

Configuration of CRC field is as shown in FIG.7.

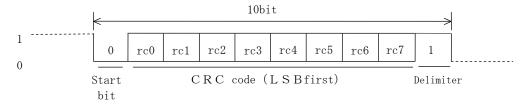


FIG.7 Configuration of CRC field

① Start bit: Fixed

② CRC code : Generation algorithm in accordance with $G(X) = X^8 + 1$ (X=

 $rc0 \sim rc7$

Data is configured in accordance with LSB first.

Calculation is executed to all bits except for Start bit and

Delimiter of fields rather than CRC.

③ Delimiter : Fixed

< CRC calculation example >

When encoder data (CF $\sim\!$ D F 3) is as shown in Table C, CRC value is following value.

Table C

		-	COLO C				
Field name	CF	SF	DF0	DF1	DF2	DF3	CRC
Encoder transmission data (LSB first)	01001100	00001000	10000000	00010100	00001101	00010000	11001101

年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	
年	月	目	$\triangle \times$	

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

6.3.5 Address field (ADF) and E²PROM field (EDF)

Configuration of ADF field is as shown in FIG.8.

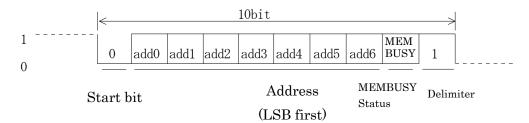


FIG.8 Configuration of ADF field

①Start bit : Fixed

②Address : E^2 PROM address (0~79) LSB first

③MEMBUSY status: Access status to E²PROM can be judged by MEMBUSY status. Relationship between MEMBUSY status and the transmission data from the encoder is referred to Table 4.

Table 4 MEMBUSY status and transmission data

	Request		Encoder transmis	sion data	内容		
	MEMBUSY	MEMBUSY ADF EDF		71			
Reading	0	0	ADF of request	E²PROM Normal data	Reading is normally executed		
, , , , ,	J	1	ADF of request	00[HEX]	Writing is in action Request for reading is invalid		
		0	ADF of request	EDF of request	Request for writing is received		
Writing	0	1	ADF of request	00[HEX]	Writing is in action Request for writing is invalid.		

When MEMBUSY status of transmission data from encoder is logical value "1", [Writing is in action.] [Writing] process based upon request is not executed.

For checking out that writing process of data into E²PROM was normally completed, the check is to be conducted by transmitting [Request for reading (data ID D)].

④Delimiter : Fixed

(5) E D F : 8 - bit data (LSB first)

Configuration of data field is in conformance with FIG.6.

-	年	月	日	$\triangle \times$
	年	月	日	$\triangle \times$
	年	月	日	$\triangle \times$

 $_{\mathrm{Ref.}}$ KMS- 9 2 Q S P 2 4

7. F	unctional	explanation	of	status	flags
------	-----------	-------------	----	--------	-------

Naming	Explanation	How to clear
BUSY flag (BUSY)	Function: Flag denotes that determination work of absolute value within single turn is under way. More specifically, when scan data of M-sequential scan data is logically wrong or three-time detected scan data are not identical, logical value "1" is outputted. All position data for this period are fixed to '0'. After determination work thereof is over, logical value is returned to "0". Detection timing: Upon normal operation (when main power is turned on) Output: Non latch	Auto clear (turn speed is lower to 300min ⁻¹ or less.)
PS error (PSERR)	Function: It is to monitor matching of "Multi-turn count block" and "Single turn count block". More specifically, ①multi-turn count amount calculated by magnetic encoder ②multi-turn count amount pseudor calculated by Carry/Borrow of absolute value within single turn Said ① and ② are compared and when (difference between ① and ②) ≧ (single turn), alarm is turned on. Detection timing: Upon normal operation Output: Latch	Turn on main power (+5 twice or, error resetting (ID7, IDC)
ST error (STERR)	Function: It is to monitor matching of "ABS block" and "INC block". More specifically, ① specific 1 bit among absolute sensors ② specific 1 bit among shift registers in ASIC (generated from M-series polynominal expression) Said ① and ② are compared and when data is different, alarm is turned on. Detection timing: In normal operation Output: Latch	Turn on main power (+; twice or, error resetting (ID7, IDC)
Overspeed alarm (OVSPD)	Function: When turn speed exceeds over 6600min¹ (MN) in normal operation, alarm is turned on. In backup operation, when it exceeds over 10000min¹ (MN), alarm is turned on. Detection timing: Upon normal operation and backup operation Output: Latch	Turn on main power (+ twice or, error resetting (ID7, IDC)
Battery alarm (BATT)	Function: When external battery voltage falls below 3.0V (TYP), alarm is turned on. Detection timing: In normal operation Output: Latch	Error resetting (ID7, IDC)
MT error	Function: When encoder is in non-operation state and cannot fulfill its function as encoder, alarm is turned on. More specifically, when higher voltage of built-in encoder backup capacitor voltage or external battery voltage falls below 2.76V (TYP), alarm is turned on. Detection timing: Upon backup rotating operation Output: Latch	Error resetting (ID7, IDC)
Overflow flag	Function: When count amount exceeds over -32766~+32765, logical value "1" is outputted. Multi-turn counter works as cyclic counter of 0~65535 even after overflowed. Detection timing: Upon normal operation and backup operation Output: Latch	Error resetting (ID7, IDC)

Note)

- \cdot As to error resetting, please read [10. Precautions in request transmission].
- When main power is turned on $(300 \text{min}^{-1}\text{or less})$ in the condition that the external battery is not connected and internal backup capacitor is fully discharged, the status is as shown below.

BUSY, PSERR, STERR	"(ď
BATT.MTERR	"1	ı,

年	月	日	$\triangle \times$					
年	月	日	$\triangle \times$					
年	月	日	$\triangle \times$					

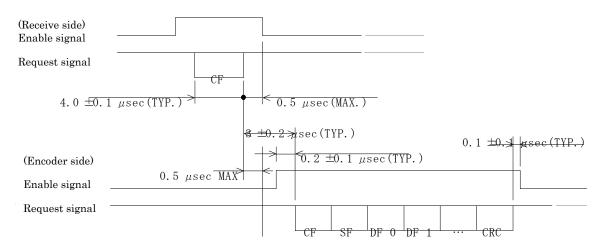
KOM	OTE	K CC).,LTD.	(MAR-M35X)		R	lef.	KMS-	9 2 Q S	SP24	1
OVF.	OVSPD)			·····"X" (indefin	ite)	1				
F		П	^ \/								
			<u> </u>								
			$\triangle \times \\ \triangle \times$								
年	月	目 .	\triangle \times								

 $|_{
m Ref.}|$ KMS- 9 2 Q S P 2 4

8. Detection of data capture frame

8.1 Detection of frame start

In control field (CF), initial logical value "0" next to idle is deemed to be frame start and when sucessive 3 bits match the synch code, said frame start is [Frame start]. When the synch code is not mached, frame detection will be executed again. In addition, data frame will be transmitted 3 μ sec.(TYP) later after receiving delimiter signal of request frame.



Internal data is latched

FIG.5 Frame detection

8.2 Frame end

Frame is deemed to be end unless there is start bit next to delimiter after frame start is detected. Thus, there is no filed specifically denoting frame end.

8.3 Idle

In between frame and frame is idle. Transmitting side has the output logic fixed to " 1 " .

8.4 Request invalid conditions

Request is invalid when the condition is as shown in Table 5. In this case, there is no data transmission from encoder.

Table 5 Request invalid conditions

No.	Conditions
1	Logic of synch code is abnormal
2	Data ID code is other than 0,1,2,3,7,8,C
3	Logic of parity is abnormal
4	Logic of delimiter is abnormal

年	月	日	Δ×
年	月	日	$\triangle \times$
年	月	日	$\triangle \times$

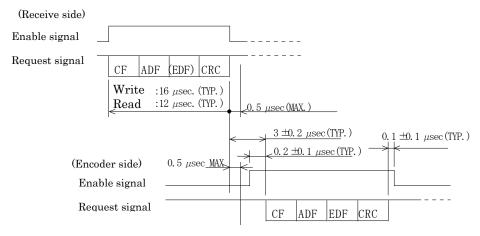
 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

9. Detection of access frame to E²PROM

9.1 Detection of frame start

Intial logical value "0" next to idle is deemed to be frame start and when sucessive 3 bits match synchro code, said frame start is [Frame start].

When synchro code is not mached, frame detection will be executed again.



Internal data is latched

FIG.10 Frame detection

* Encoder starts data transmission $3\,\mu$ sec. (TYP) later after receiving request for access to E²PROM (data ID 6 and 7). However, it should be noted that the data returned of request for writing (data ID 6) denotes reception of data ID 6 and writing process is not always completed. Data writing process takes 18m sec. (MAX) to complete after receiving request.

9.2 Frame end

Frame is deemed to be end unless there is start bit next to delimiter after frame start is detected. Thus, there is no filed specifically denoting frame end.

9.3 Idle

In between frame and frame is idle. Output on transmitting side is fixed to logical value "1".

9.4 Request invalid conditions

Request is invalid when the condition is as shown in Table 6. In this case, there is no data transmission from encoder.

Table 6 Request invalid conditions

No.	No. Conditions				
1	1 Logic of synch code is abnormal.				
2 Address other than area open to user is specified.					
3	Data ID code is other than ID 6 and D.				
4	Logic of parity is abnormal.				
5	Logic of delimiter is abnormal.				
6	Logic of CRC is abnormal.				

年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

10. Precautions in request transmission

Function	Data ID	Precaution
Data capture	0,1,2,3	In accordance with data field chart (Table 2), data ID code (Table 1) is to be transmitted to encoder. Encoder is loaded with RS485 receiver IC, so data ID code should be transmitted by driver IC (equivalent of SN65LBC176) in conformity with RS485.
Single turn data resetting	8	Data ID is to be sucessively transmitted to encoder 10 times at transmission intervales of 40 μ sec. or more when shaft is stopped. \divideontimes Single turn data is reset to mechanical angle 0°. Once shaft position is reset, its position is kept even after main power is turned off, whether external battery is connected or not.
Multi-turn data And all errors resetting	C	Data ID is to be sucessively transmitted to encoder 10 times at transmission intervales of 40 μ sec. or more. $*$ Multi-turn data is reset.(Single turn data is not reset.) In addition, all latch errors(Overspeed, Overflow flag, PS error, ST error and MT error) are reset at the same time.
All errors resetting	7	Data ID is to be successively transmitted to encoder 10 times at transmission intervales of 40μ sec. or more. \ref{Model} All latch errors(Overspeed, Overflow flag, PS error, ST error and MT error) are reset at the same time.
Access to	6	8-bit user data is writable into specified address. After data writing, it is recommendable to specify [data ID D] and check that writing was normally executed.
E ² PROM	D	8-bit user data is readable from specified address. Transmission of request for reading is referred to paragraphs 6.2.3 as well as 6.3.5.

- ** Encoder returns data shown in Table 2 every one request is received. However, Error information about the data returned will not be reset until resetting is executed.
 - When the Error is reset by data ID C or ID 7, resetting should be executed with the encoder shaft being stopped (turn speed 300min⁻¹ or less). In addition, after 10-time reception, the encoder outputs BUSY flag for max 2ms in order to operate resetting process.
 - Single turn resetting takes 18m sec. (Max) to execute the resetting after 10-time ID 8 is received because the process of writing data into E^2PROM is executed.

年	月	日	Δ×
年	月	日	$\triangle \times$
年	月	日	$\triangle \times$

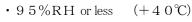
 $_{\mathrm{Ref.}}$ KMS- 9 2 Q S P 2 4

11. Environmental Conditions

11.1 Temperature

• Operation : Base plate temperature $-20\% \sim 85\%$ • Non-operation : Base plate temperature $-20\% \sim 85\%$

11.2 Humidity (non condensing)



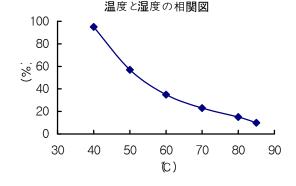
• 5 7 % RH or less
$$(+50^{\circ}\text{C})$$

•
$$35\%RH$$
 or less $(+60\%)$

•
$$23\%$$
RH or less $(+70\%)$

• 1 5 % RH or less
$$(+80^{\circ}\text{C})$$

• 10 % R H or less
$$(+85^{\circ}\text{C})$$



Detail is referred to diagram at right.

11.3 Vibration 3000Hz or less at 98m/sec²

Direction: X, Y and Z directions Time: 48 hours in each direction

Frequency: Non damage at 100 \sim 3000Hz/Sweep 15min

11.4 Impact 980m/sec^2 6m sec. or less

Direction: X, Y and Z directions

Number of impacts: Non damage at a rate of 10-time impacts or less in each direction

11.5 Noise resistance

No displacement occurs in noise tests below.

(1) Power supply noise

Common mode (between frame and power supply)

Normal mode (between 0V and 5V)

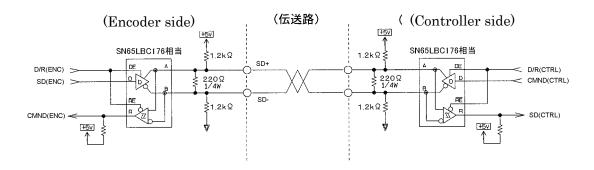
 $\pm 100 \mathrm{V}$ for both modes (pulse width 50n sec and 1 μ sec; polarity \pm)

- (2) Cable radiation noise 2kV (pulse width 50n sec and 1 μ sec; polarity \pm)
- (3) Magnetic noise 0.01 tesla

年	月	日	$\triangle \times$
年	月	日	$\triangle \times$
午	Я		\wedge \vee

 $_{\mathrm{Ref.}}$ KMS-92QSP24

1 2. Transmission/reception circuit configuration examples



Do not transmit request signal to encoder while encoder is transmitting data. If data is inadvertently transmitted, encoder I/O may be damaged.

When data is being transmitted from encoder, normally encoder is in reception mode.

13. Input/output signals

Pin number	Function	Remarks
1	VB	Battery power (Note 1)
2	SD	Carial data airead
3	/SD	Serial data signal
4	VCC	Main power DC+5V $\pm 5\%$
5	GND	Signal ground
6	GND	Signal ground
7	N.C.	(Unassigned)

Note 1) External battery power is required for backup operation of encoder.

As to status flag when external battery power is not connected and main power is turned on with built-in capacitor fully discharged, please see [7. Functional explanation of status flags].

13.1 Base plate connector (receptacle and header)

 \cdot Model name : 8-917570-7 (made by AMP, $\,$ 7- Pole 1.5mm mini VP connector)

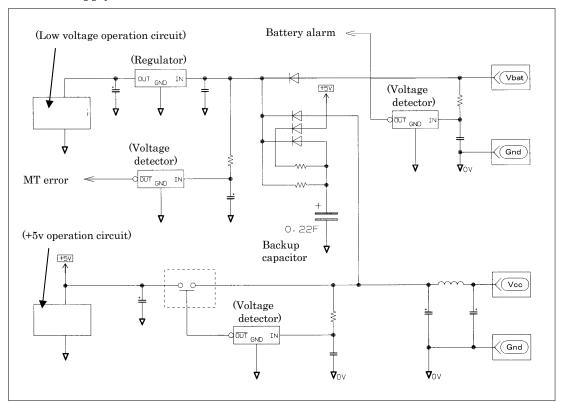
*Adaptable connector

Plug housing: 917572-7 Pin contact: 917571-4

年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	

 $_{\mathrm{Ref.}}$ KMS- 9 2 Q S P 2 4

1 4. Power supply circuit (for reference)



15. Serial number plate

15.1 Location for serial number plate

On side facet of mold (please refer to diagram at right)

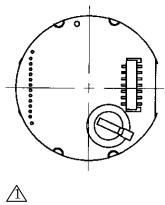
- 15.2 Descriptive items
 - (1) Model name: 35XN51

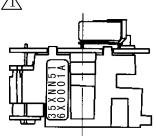
([3] in sixth digit from right shows that the product complies with RoHS Directive.)

(2) Product serial number: 6X0001A

First digit from left corresponds to last one digit of a production year, second digit a production month (Oct.: X, Nov.: Y and Dec.: Z) and then serial number comes in four digits and alphabet (A, B, $C \cdot \cdot \cdot$) showing design revision record comes after 4-digit serial number.

15.3 Plate material Paper label





年	月	日	$\triangle \times$
年	月	日	$\triangle \times$
年	月	日	$\triangle \times$

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

16. Signal adjustment standard

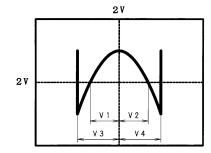
Signal	Item	Condition	Adjustment standard value (upon shipment from Nikon) (*)	Final standard value (on finish product)
	Amplitude (VA,VB)	700/6000 min ⁻¹	$2.0\sim3.0$ Vpp: 700 min^{-1} $1.4\sim3.0$ Vpp: 6000 min^{-1}	1.9~3.2Vpp : 700 min ⁻¹ 1.3~3.2Vpp : 6000 min ⁻¹
Incremental signal (A,B)	Level (EA, EB)	700 min ⁻¹		
SIGNAL (ILD)	Phase difference (\$\phi\$)	700 min ⁻¹	90±10 degrees	90±10 degrees
	Amplitude (V1,V2)	700 min ⁻¹	0.75V or more	0.7V or more
Absolute	Center level (V1-V2)	700 min ⁻¹	±0.2V or less	±0.3V or less
signal (ABS)	Amplitude (V3)	700 min ⁻¹		
	Amplitude (V4)	700 min ⁻¹		
Multi-turn	Phase (a, b, c, d)	700 min ⁻¹	1/4±1/10P	1/4±1/8P
signal (MA、MB)	Duty	700 min ⁻¹	1/2±1/20P (50±5%)	1/2±1/20P (50±5%)

*) This value is applied to any combination of base plate and disk upon shipment from Nikon.

VA,VB:A phase, B phase amplitude (peak to peak voltage)

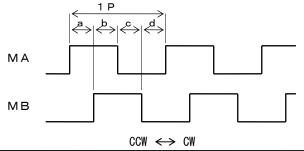
EA,EB: A phase, B phase level (2V standard)

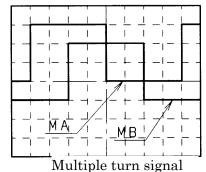
ABS-A Lissajous signal



X-axis: ABS signal (0V standard)
Y-axis: A signal (0V standard)

Multi-turn signal (MA, MB)





 年 月 日 △×

 年 月 日 △×

 年 月 日 △×

 $|_{
m Ref.}|$ KMS- 9 2 Q S P 2 4

17. Assembling adjustment procedure

Wrist strap has to be used when working on assembly. Moreover, power supplies like adjustment tools are sure to be grounded.

17.1 Mounting pulse disk

Spacer is inserted so that there is spaced at 4.12 ± 0.03 between motor datum face and shaft under surface of pulse disk, and then pulse disk is mounted to motor shaft. Adhesive agent (thread locking agent) is applied to hexagon socket set screws (M3 x 4) and the set screws are tightly fastened by clamp torque $0.49N \cdot m$.

Beware of handling pulse disk.

- Disk should be treated carefully so that it is not bended.
- Disk should be treated carefully, otherwise you may be injured.

17.2 Mounting base plate

Base plate is mounted to motor so that it does not come into contact with pulse disk and adhesive agent (thread locking agent) is applied to thread portion of hex socket head cap screws M2.6 X 6, and the head cap screws are temporarily tightened by screw and M2.6 flat washer.

Recommended adhesive agent: LOCTITE 242

17.3 Signal adjustment

- Encoder is connected to counter and signal switching tool, and the probe for taking signal is mounted to base plate.
- Counter and signal switching tool should not be turned on until encoder power cable and probe for taking signal both are securely mounted.

If counter and signal switching tool are turned on with only the probe mounted, encoder may be damaged.

- Signal switching tool is switched over to ABS A, motor turn speed is set to 700min⁻¹ and then sensor is aligned. The alignment of the sensor is conducted in the radius direction of the base plate so that convex portion of signal waveform is overlapped as much as possible and the signal becomes large in the X and Y directions.
- $\boldsymbol{\cdot}$ After the sensor is aligned, screws are fastened by torque 0.49N $\boldsymbol{\cdot}$ m and secured.
- Be sure to check out that upon excitation of the motor, display of ST in the counter falls within the standard.

年	月	日	$\triangle \times$
年	月	日	$\triangle \times$
年	月	日	$\triangle \times$

 $_{\mathrm{Ref.}}$ KMS- 9 2 Q S P 2 4

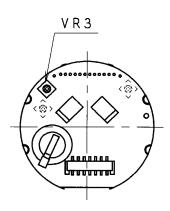
17.4 CS adjustment (when M35 counter is used)

Original point is set, depressing down ST-CLR_SW presetting absolute position within single turn of the counter with the motor excited.

17.5 Signal check

· absolute signal (ABS)

The signal switching tool is switched over to ABS • A. Oscilloscope is switched over to lissajous to check out the amplitude of V1, V2 and |V1-V2|. The adjustment of |V1-V2| is made by volume (VR3). As to the location of VR3 and the signal standard, please refer to diagram at right and [14. Signal adjustment standard], respectively.



1) The volume adjustment is required to make under load of 4.9 [N] or less.

2) Following recommended driver is required to use for volume adjustment.

Recommended driver: VESSEL-made Ceramic adjustment driver No.9000 (-1.8×30)

* VR adjustment should be made so that the driver does not come into contact with any of parts other than VR.

If there is any crack in the contacted parts, please stop use of the product in question. $\label{eq:product}$

• Incremental signal (A, B)

The signal switching tool is switched over to A and B to check out that the signal falls within the standard.

• Multi-turn signal (MA, MB)

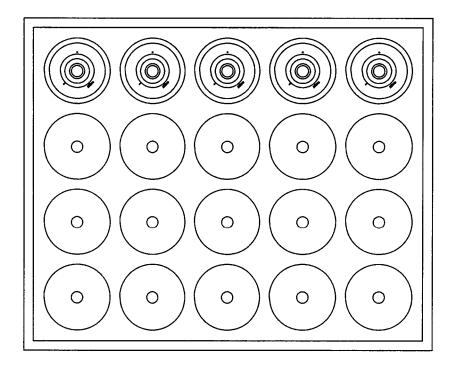
The signal switching tool is switched over to MA and MB to check out that the signal falls within the standard. The check must be conducted in both directions of CW and CCW.

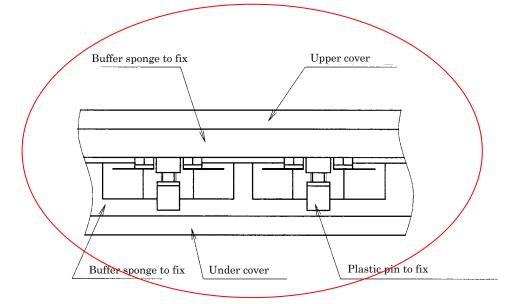
年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

18. Packing specifications

18.1 Pulse disk (Returnable container)





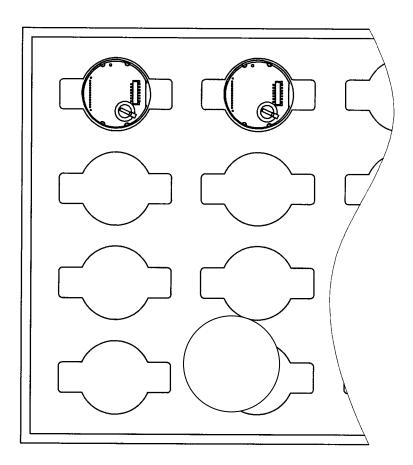
When transported, this returnable container is housed into cardboard box. Following buffer material and lid must be used.

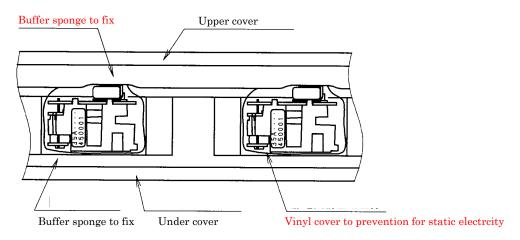
- Buffer material: Lightlon® board 25 fold (antistatic material)
- · Lid: Cardboard plastic

年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	

 $_{
m Ref.}$ KMS-92QSP24

18.2 PWB (returnable container)





When transported, this returnable container is housed into the cardboard box.

18.3 Other parts

Other parts except for the disk and base plate are delivered in bags.

年	月	日	$\triangle \times$
年	月	日	$\triangle \times$
年	月	日	$\triangle \times$

 $_{\mathrm{Ref.}}$ KMS- 9 2 Q S P 2 4

19. Precautions

19.1 Usage precautions

In order to prevent product failure and malfunction, important precautions below should always be followed when using this product.

<u>Precautions in keeping • transportation • installation</u>

This product should not be kept or installed in the following environment.

- · Area or place exposed to direct sunshine
- Area or place where ambient temperature exceeds over temperatures for keeping and installing condition
- Area or place where relative humidity exceeds over humidity for keeping and installation condition
- Area or place where temperature suddenly changes and thereby dew condensation is generated
- Area or place adjacent to corrosive gas^{*1} (such as hydrogen sulfide, sulfurous acid, hydrochloric acid, ammonia, etc) and flammable gas
- · Area or place with many dusts, salts and metallic powders
- · Area or place which is soaked by water, oil and chemical, etc
- · Area or place where excessive vibration and/or impact get through the body
 - %1 Rather than ambient normal environment, volatilization of ingredients like grease, etc applied around the encoder may generate the corrosive gas. Much attention to the corrosive gas is required.

Precautions in mounting

- Pay careful attention to the installing location so that the oil, foreign object, etc never get inside the encoder.
- The pulse disk requires careful handling. Especially, the foreign object should not be adhered to the disk and the disk should not be bended. Furthermore, since the edge of the disk is sharp in form, the disk should be carefully handled so as not to injure the hand and/or palm.
- Antistatic measures in the installation environment are ensured to take so that the overvoltage is not applied to electric parts.
- Careful attention to the installing location has to be paid since the vibration and/or impact against the encoder account for the malfunction.
- Since the noise passed on to the encoder causes the malfunction, mounting environment must be fully checked out when housing the encoder into the case and routing the motor cable for wiring.
 - *1. As for the encoder housing case, the metallic case capable of electromagnetic shield should be used to maintain the stabilized electric potential. In this case, aerial spacing between the encoder and the electric parts in the base plate should be sufficiently secured. In addition, when the housing case undergoes magnetic field from the motor adjacent to the encoder and/or the magnetic field arising from welding current, as a material of the encoder housing case, the soft magnetic body (like soft iron) should be used.

年	月	日	$\triangle \times$
年	月	日	$\triangle \times$
年	月	目	$\triangle \times$

 $|_{
m Ref.}|$ KMS- 9 2 Q S P 2 4

*2. The motor power line should not be gotten close to the encoder.

3. FG line of motor and FG of the machinery are ensured to be grounded.

·Once the encoder is mounted, the encoder must be fully evaluated as a system ahead of usage.

Precautions in wiring

- · Wiring should be properly and surely carried out.
- · Wiring should be carried out with power turned OFF.
- Specified power voltage should be used for wiring. Decrease in voltage of the power due to the wiring length should be taken into consideration, too.
- Encoder wiring and other power lines should not be guided through the same duct and/or not used, binding them in parallel with each other.
- Twisted pair line should be used for signal line and power supply line of the encoder wiring.
- A cable for encoder wiring should use braided shield wire. Also, the braided shield wire is ensured to be grounded both on the encoder side and opponent side (controller side).

年	月	日	$\triangle \times$	
年	月	日	$\triangle \times$	
年	月	В	$\triangle \times$	

 $_{
m Ref.}$ KMS- 9 2 Q S P 2 4

Operational precautions

- The system should not be run until the system safety design with regard to the encoder failure and/or malfunction is fully reviewed and checked in advance.
- When alarmed, the cause is to be removed and once the safety is secured, the alarm is rest to resume the operation.
- Do not strain the cable.

19.2 General precautions

- This product is designed to be used by being incorporated into a part of the general electronic equipment (like office automation machinery, communication equipment, electronic home appliances, amusement equipment, measuring equipment, general industry-use equipment, etc). It should be noted that this product is not intended for use in application requiring extremely high reliability as well as safety (such as transport equipment, aircraft space instruments, nuclear energy control system, medical equipment for life saving, etc).
- Nikon is keeping on making utmost effort to improve the product quality and reliability, but generally speaking, the malfunction of the semiconductor chips and/or the occurrence of the product failure cannot be completely prevented. Well before starting use of this product, users are requested to think up the safety measures for the purpose of avoiding the possible troubles, considering possible situations arising from the product malfunction, etc. If the malfunction, failure and/or product life related to this product damage or adversely affect other person's life or property, or the equipment, facility or machine tool incorporating or utilizing this product breaks down, Nikon shall not be liable for any product malfunction, failure and/or life, whether its nature or extent.

It should be noted that it is your responsibility to prepare the safety design for the entire system on yourself.

20. Design revision record

Revision	What is revised

年	月	日	Δ×
年	月	日	$\triangle \times$
年	月	日	$\triangle \times$