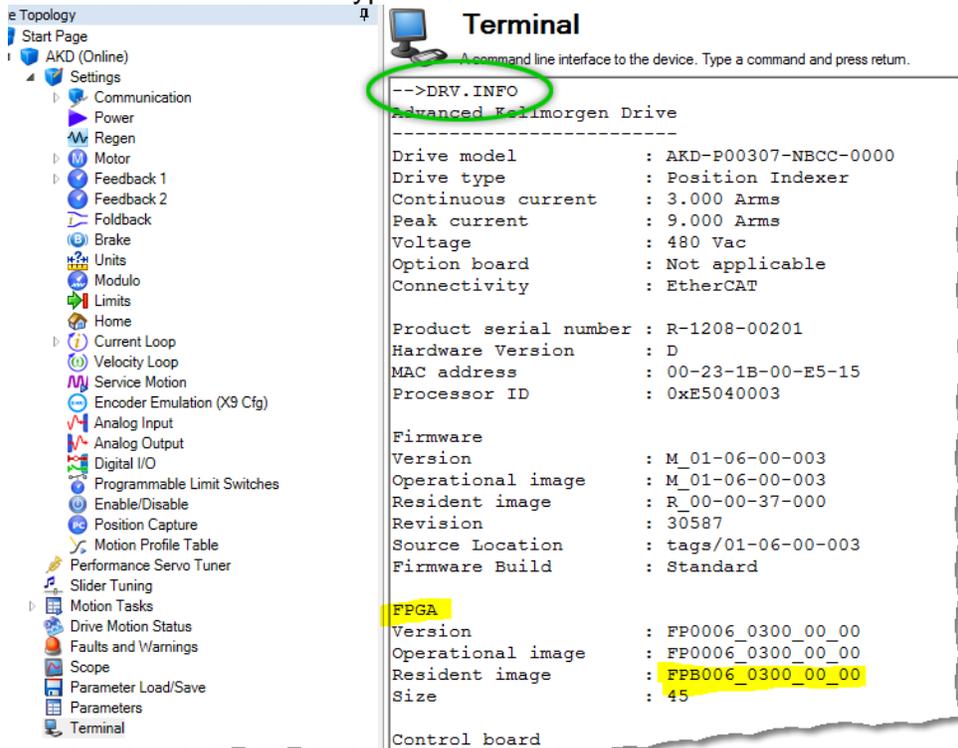


4/20/2017

The following steps may be taken to update the Backup FPGA on AKDs.

1. Connect to your drive with Workbench.
2. Select the terminal and type **DRV.INFO** then execute the command.

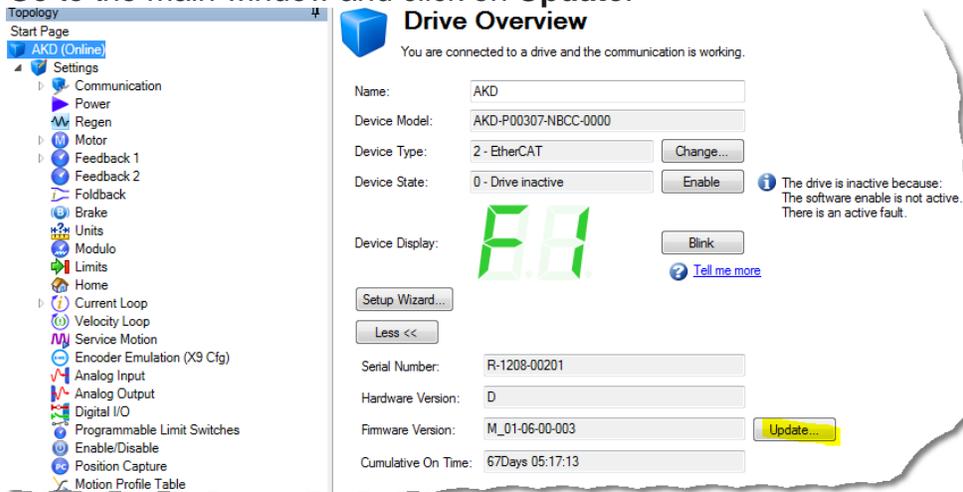


In this example the Backup FPGA version can be identified by looking for the "FPGA" section and "Resident image" alias Backup FPGA.

The coding for the backup FPGA version is as follows: "FPB00x_yyyy_yy_yy", where "x" represents a certain FPGA type and "y" represents the version number. The upper number represents the most significant version number. All versions lower than "0602.00.01" are affected.

In this example the version is "0300.00.00", which is a backup FPGA version with the brake issue.

3. Go to the main window and click on **Update**.



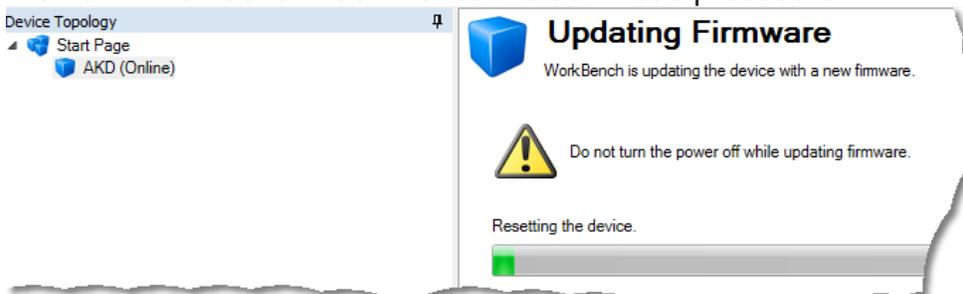
4. Choose the correct Backup FPGA to download, which matches your drive type. Note see [Backup FPGA v0602 00 01.zip](#). Click **Open**

5. Workbench filters the correct type matching your drive type.

In this example the drive type is **AKD-P-00307-NBCC-0000**, where the bold characters need to match the file name. Workbench filters the correct file to be AKD-P-NBCC-V0602-00-01-R-FPGA.i00.

Name	Date	Type	Size
AKD-P-NBCC-V0602-00-01-R-FPGA.i00	04.04.2017 10:24	I00-Datei	3.425 KB

6. Wait until Workbench has finished the download procedure.



7. Verify that the upgrade of the Backup FPGA was successful by repeating steps 2 and 3. Check that the version is 0602_00_01. If the version matches, the brake issue in the Backup FPGA is resolved.

```
FPGA
Version           : FP0006_0602_00_00
Operational image : FP0006_0602_00_00
Resident image    : FPB006_0602_00_01
Size              : 45
```